

FIG. 1
(Prior Art)

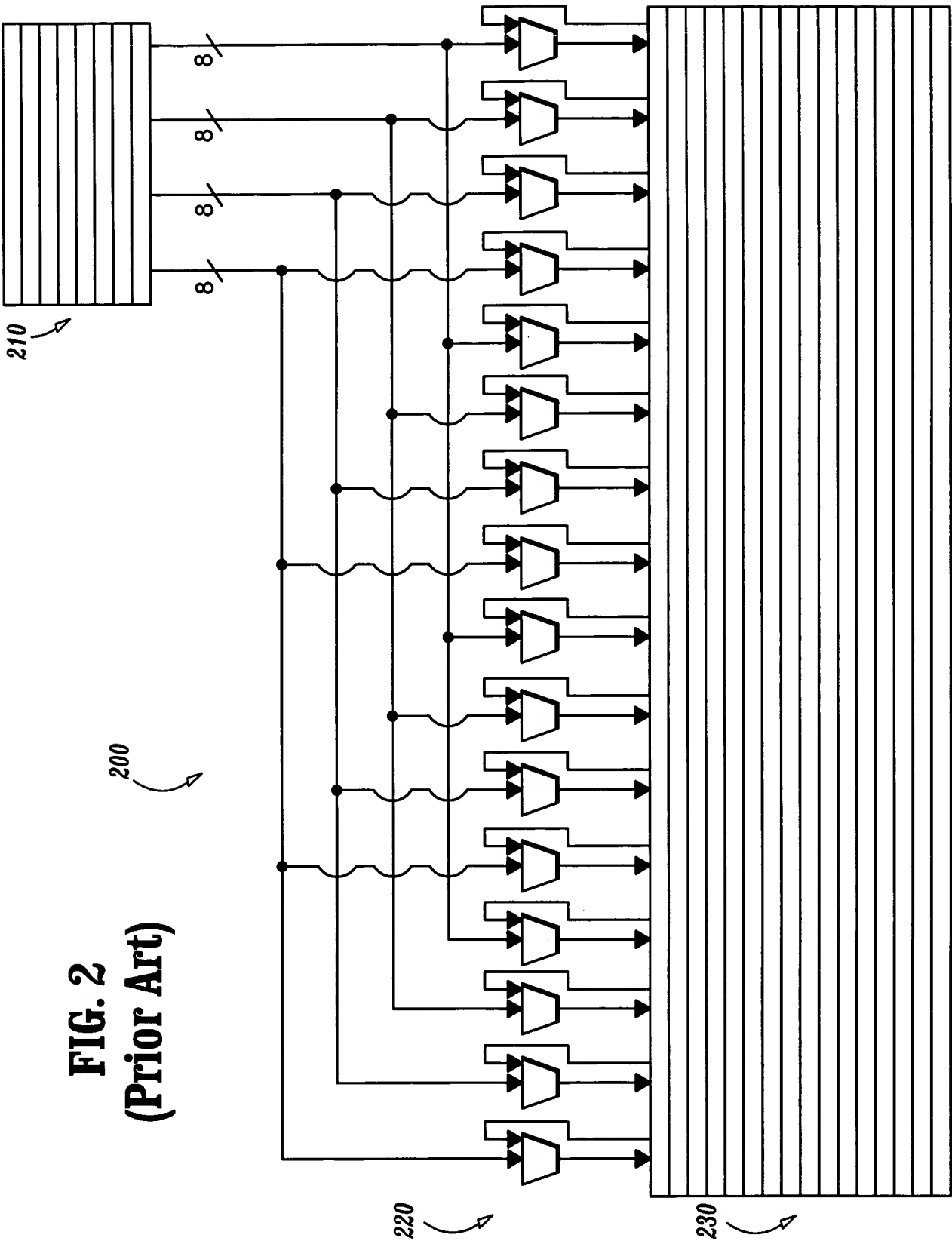
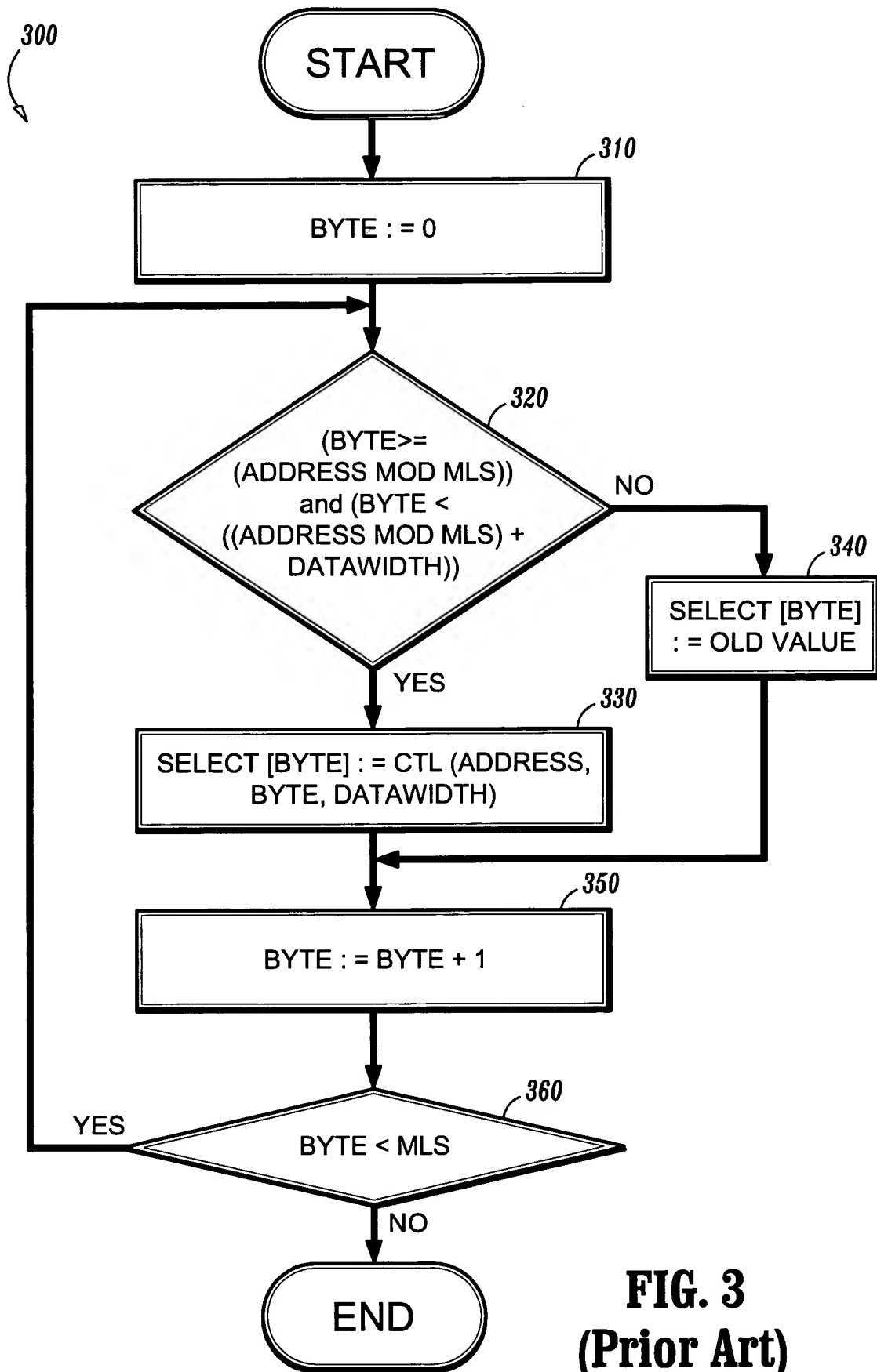
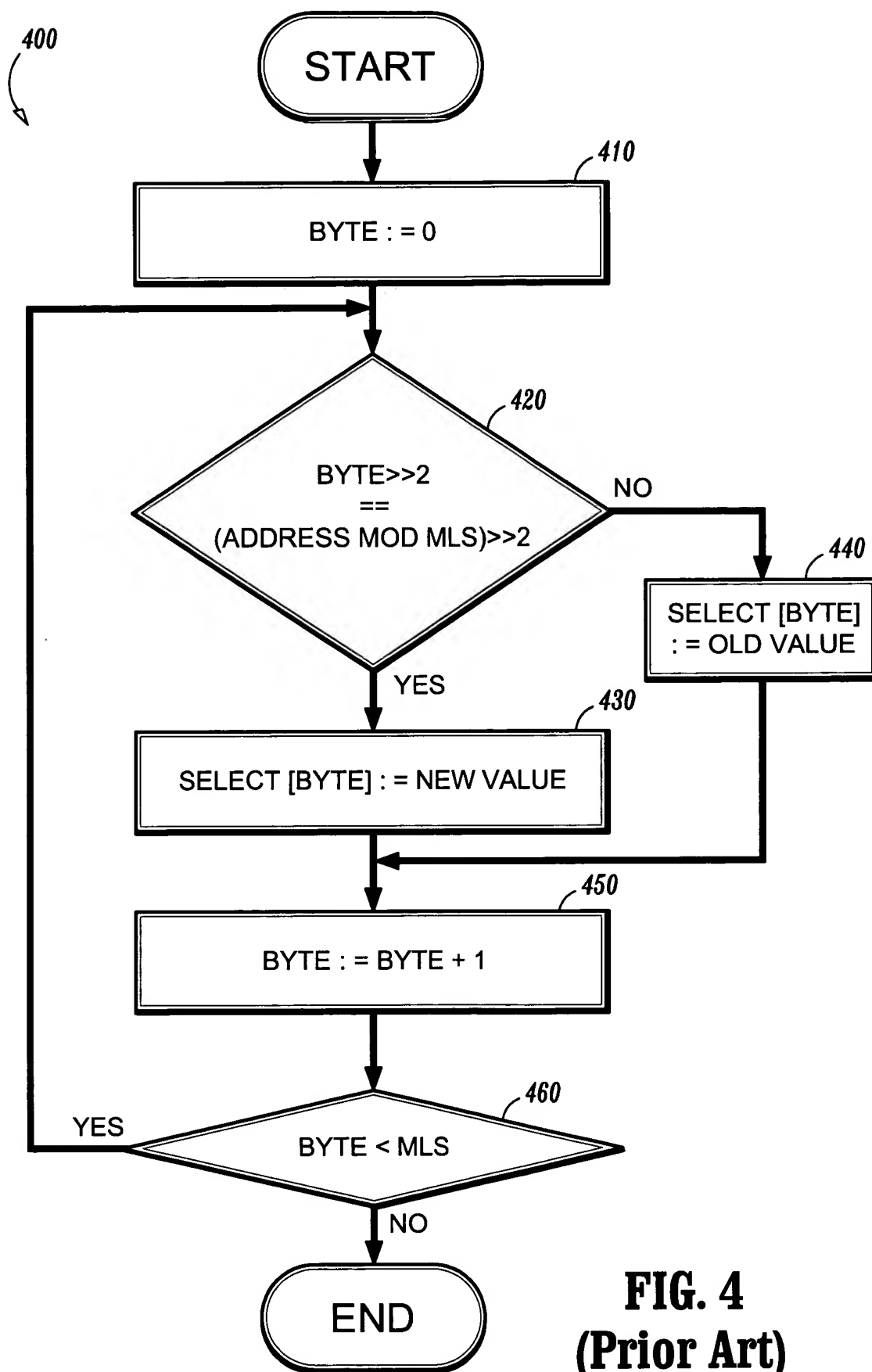


FIG. 2
(Prior Art)





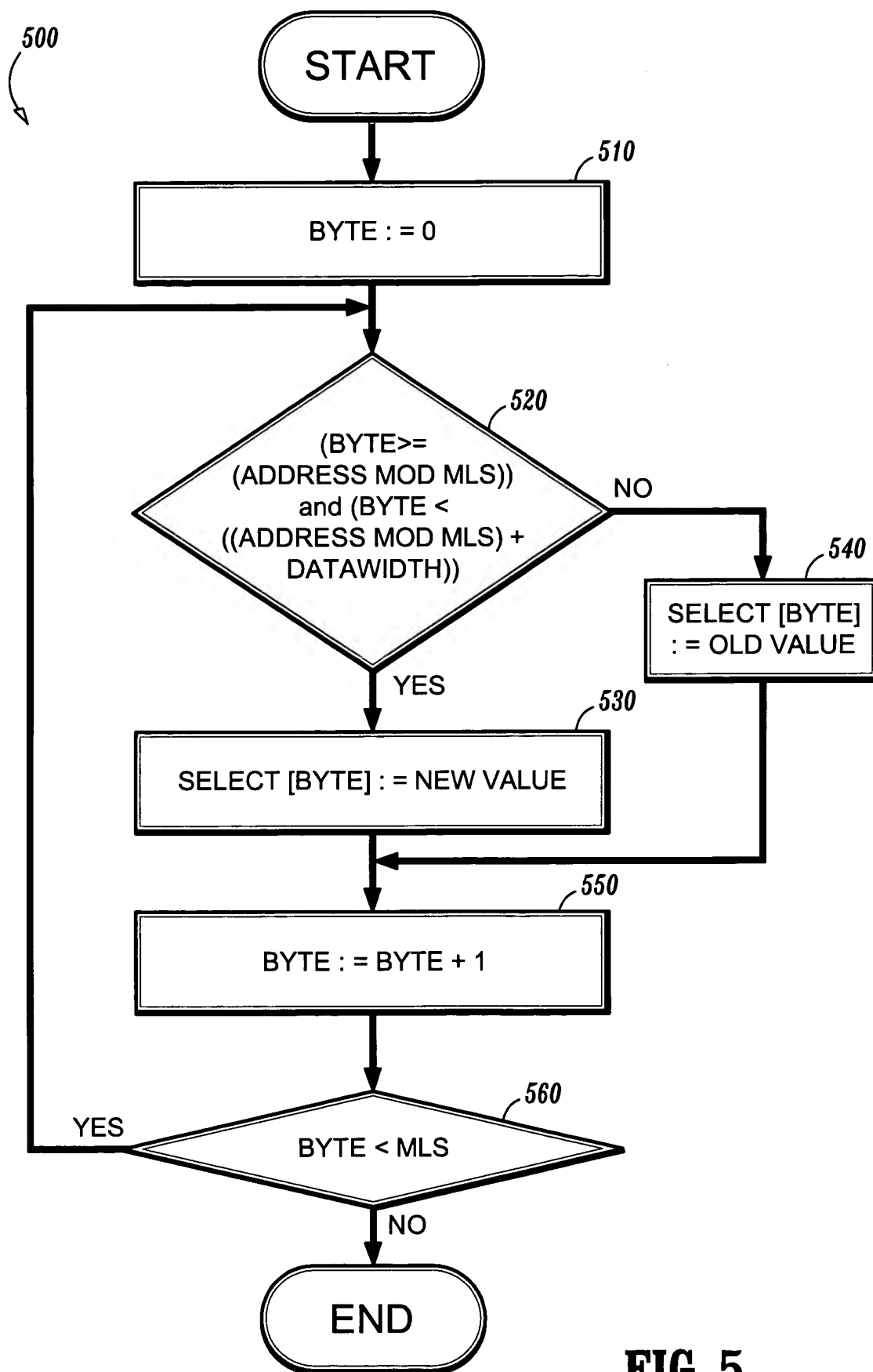


FIG. 5

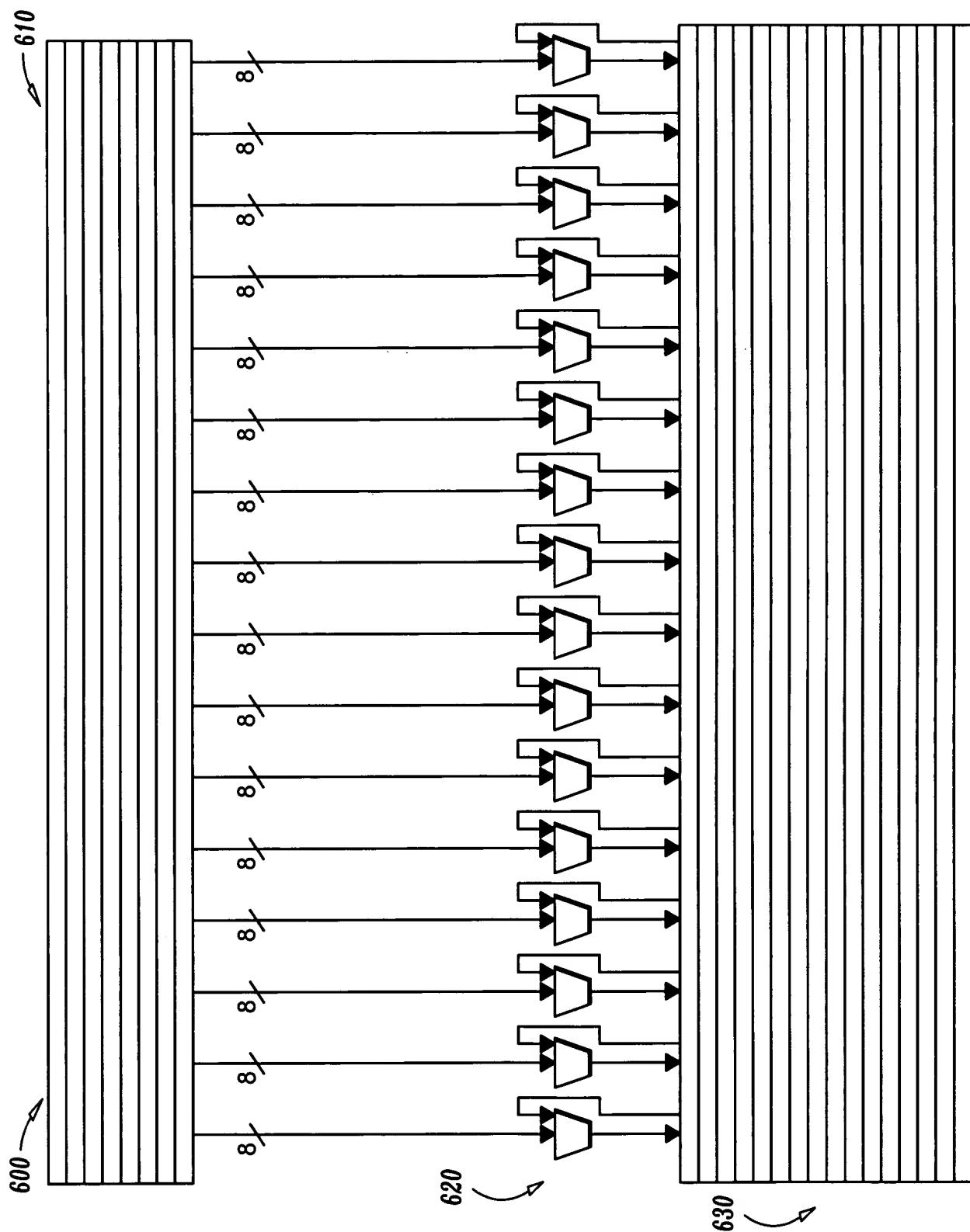
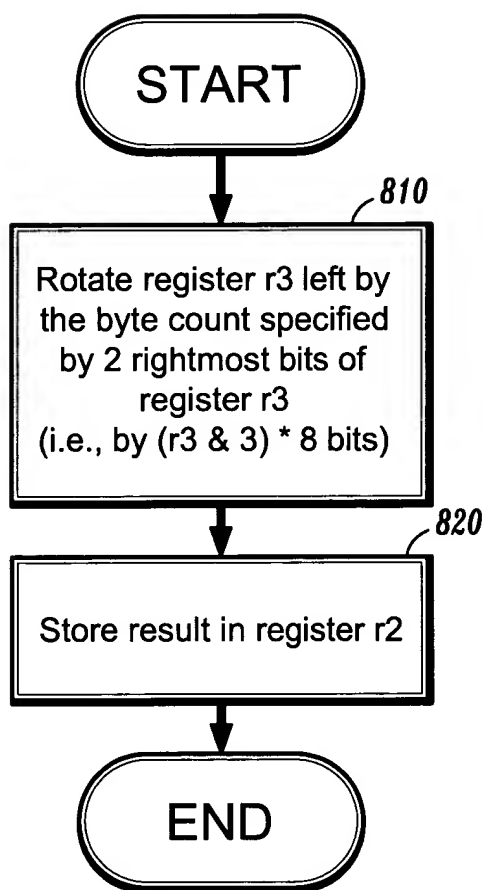
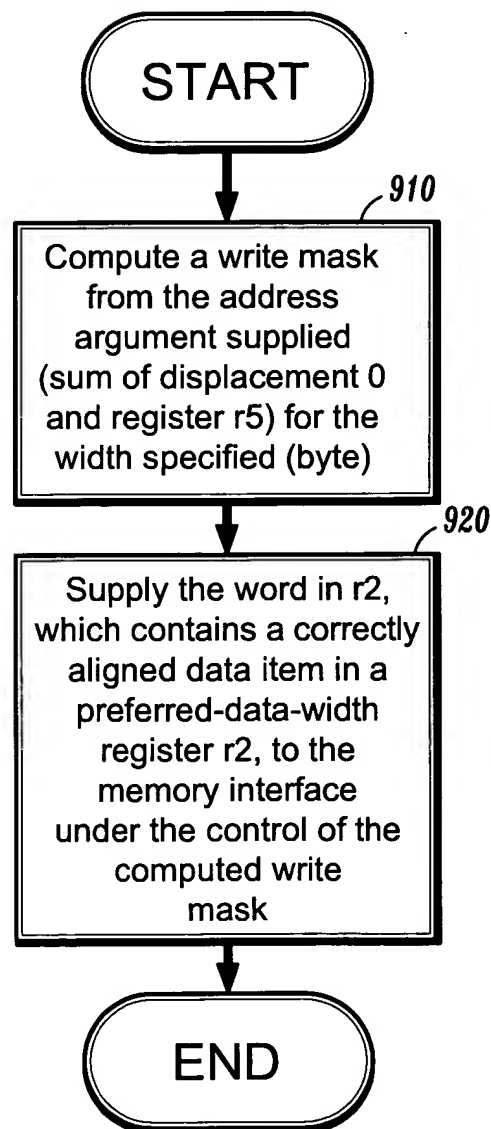
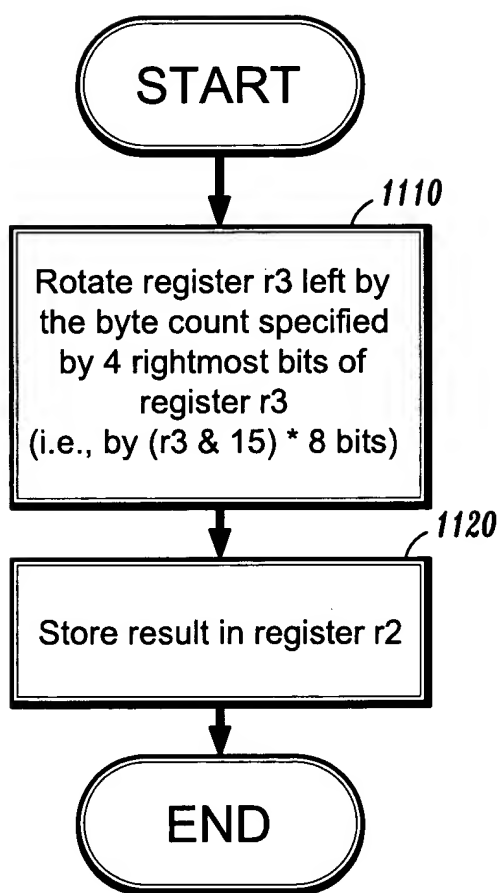
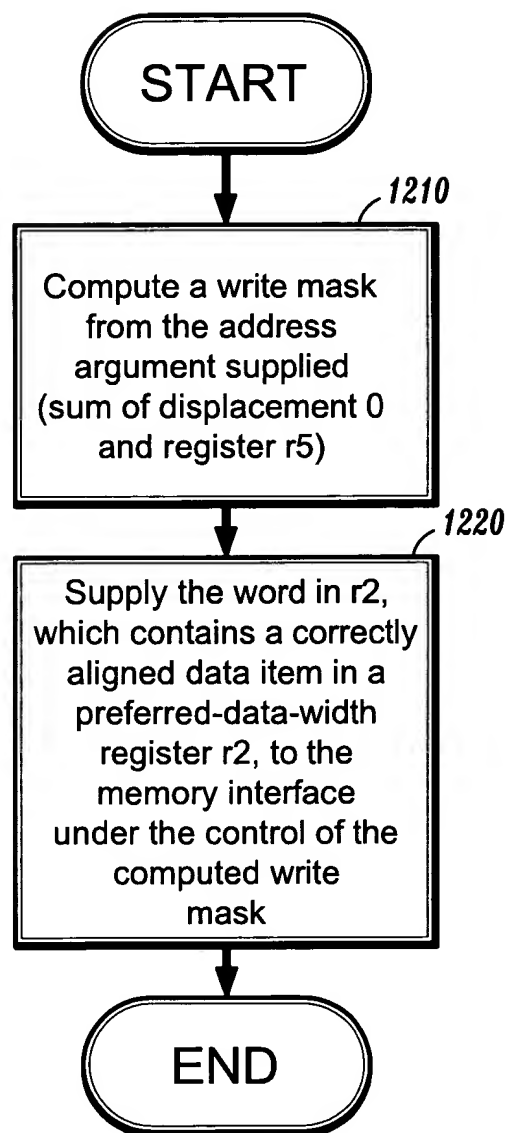


FIG. 6

subfi r3, r5, 3 ;	compute rotate count	$r3 = 3 - r5$
rotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
stb r2, 0(r5)	; store byte	mem[r5] = r2 using a 1-byte mask aligned with address 0(r5)

FIG. 7**FIG. 8
(Prior Art)****FIG. 9**

subfi r3, r5, 12	; compute rotate count	$r3 = 12 - r5$
vrotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
vstw r2, 0(r5)	; store word	mem[r5] = r2 using a 1-word (4-byte) mask aligned with address 0(r5)

FIG. 10**FIG. 11
(Prior Art)****FIG. 12**